Abstracts

An ultra low current consumption two step power control driver MMIC with self current control for wideband-CDMA handsets

M. Nakayama, K. Motoyoshi, T. Kitazawa, K. Tara and M. Hagio. "An ultra low current consumption two step power control driver MMIC with self current control for wideband-CDMA handsets." 2000 Radio Frequency Integrated Circuits (RFIC) Symposium 00. (2000 [RFIC]): 97-100.

This paper presents a two step power control driver MMIC for wideband-CDMA handsets. It has the self current control function coupled with the output power level. It realizes low current consumption of 12 mA at an average output power level (Pave) without any sacrifice of high performance at the high output power level. It greatly contributes to the realization of compact and low power consumption W-CDMA handsets.

❑ Return to main document.